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(71) Applicant (for all designated States except US): **ACORN
PACKET SOLUTIONS, LLC** [US/US]; 2010 Corporate
Ridge, Suite 700, McLean, VA 22102 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **SKERRITT, Michael**
[US/US]; c/o Acorn Packet Solutions, LLC, 2010 Corpo-
rate Ridge, Suite 700, McLean, VA 22102 (US).

(74) Agents: **SHAPIRO, Mitchell, W. et al.**; Miles & Stock-
bridge P.C., 1751 Pinnacle Drive, Suite 500, McLean, VA
22102 (US).

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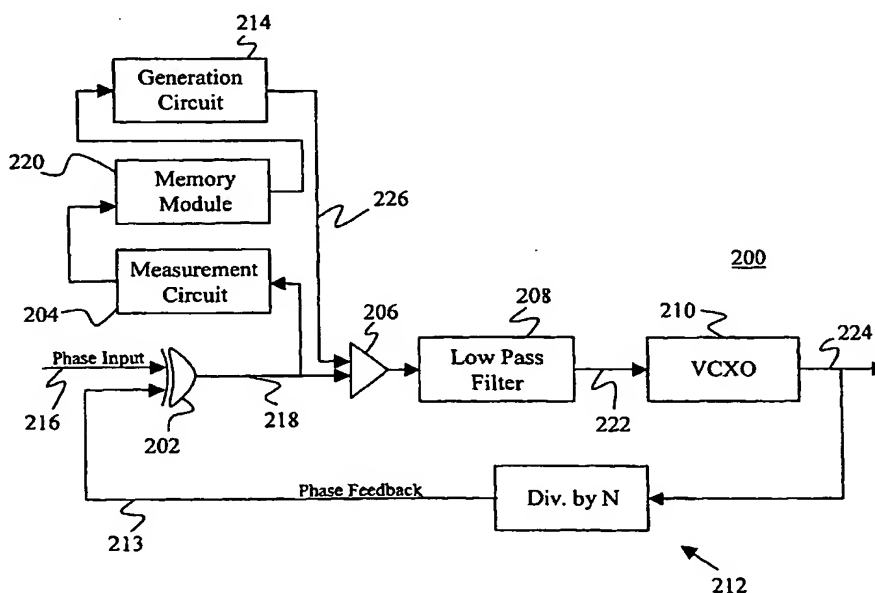
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(54) Title: **SYSTEM AND METHOD FOR MAINTAINING AN ACCURATE FREQUENCY ON A VOLTAGE CONTROLLED
OSCILLATOR**



(57) Abstract: A method for phase-locking a voltage controlled oscillator is disclosed. The method comprises receiving, at a phase detector, a phase input signal and a phase feedback signal from the voltage controlled oscillator; measuring a pulse width property of an error signal output from the phase detector to obtain a pulse width property measurement; storing the pulse width property measurement in a memory; and generating a new signal from the stored pulse width property measurement to phase-lock the voltage controlled oscillator. The method of the present invention may be used to calibrate a clock, in clock holdover and in qualification of clock sources.

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